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8954959

Basic Patent (No,Kind,Date): JP 1265348 A2 19891023 <No. of Patents: 003>

PATENT FAMILY:

JAPAN (JP)

Patent (No,Kind,Date): JP 1265348 A2 19891023

GRAPHIC PROCESSOR (English)

Patent Assignee: HITACHI LTD

Author (Inventor): KATSURA AKIHIRO; KOJIMA SHINICHI; KURAGAMI NORIYUKI

Priority (No,Kind,Date): JP 8893448 A 19880418

Applic (No,Kind,Date): JP 8893448 A 19880418

IPC: * G06F-013/36; G06F-012/00; G06F-015/64; G06F-015/66

JAPIO Reference No: ; 140025P000115

Language of Document: Japanese

Patent (No,Kind,Date): JP 95036162 B4 19950419

Priority (No,Kind,Date): JP 8893448 A 19880418

Applic (No,Kind,Date): JP 8893448 A 19880418

IPC: * G06F-012/00; G06F-013/36; G06T-001/00; G06T-001/60

Derwent WPI Acc No: * G 90-382850

JAPIO Reference No: * 140025P000115

Language of Document: Japanese

UNITED STATES OF AMERICA (US)

Patent (No,Kind,Date): US 4975857 A 19901204

GRAPHIC PROCESSING APPARATUS UTILIZING IMPROVED DATA TRANSFER TO REDUCE
MEMORY SIZE (English)

Patent Assignee: HITACHI LTD (JP)

Author (Inventor): KATSURA KOYO (JP); KOJIMA SHINICHI (JP); KURAKAMI
NORIYUKI (JP)

Priority (No,Kind,Date): JP 8893448 A 19880418

Applic (No,Kind,Date): US 302332 A 19890127

National Class: * 364518000; 340798000; 364521000

IPC: * G06F-015/20

Derwent WPI Acc No: ; G 90-382850

Language of Document: English

UNITED STATES OF AMERICA (US)

Legal Status (No,Type,Date,Code,Text):

US 4975857 P 19880418 US AA PRIORITY (PATENT)
JP 8893448 A 19880418

US 4975857 P 19890127 US AE APPLICATION DATA (PATENT)
(APPL. DATA (PATENT))

US 4975857 P 19890405 US AS02 ASSIGNMENT OF ASSIGNOR'S
INTEREST
HITACHI, LTD., 6, KANDA SURUGADAI 4-CHOME,
CHIYODA-KU, TOKYO, JAPAN, A CORP. OF ;
KATSURA, KOYO : 19890222; KOJIMA, SHINICHI :
19890222; KURAKAMI, NORIYUKI : 19890222

US 4975857 P 19901204 US A PATENT

US 4975857 P 19930713 US RF REISSUE APPLICATION FILED
(REISSUE APPL. FILED)
921203

US 4975857 P 20000627 US RF REISSUE APPLICATION FILED
(REISSUE APPL. FILED)
20000328

LEVEL 1 OF 1 PATENT

4,975,857

<=2> GET 1st DRAWING SHEET OF 36

Dec. 4, 1990

LEXIS-NEXIS

Library: PATENT

File: ALL

Graphic processing apparatus utilizing improved data
transfer to reduce memory size

REISSUE: Reissue Application filed Mar. 28, 2000 (O.G. Jun. 27, 2000) Ex. Gp.:
2763; Re. S.N. 09/536,646

Reissue Application filed Dec. 3, 1992 (O.G. Jul. 13, 1993) Ex. Gp.: 2301

CORE TERMS: memory, cycle, display, bit, graphic, processing, column, row,
buffer, pixel...

4,975,857 OR 4975857

LEXIS-NEXIS
Library: PATENT
File: CASES

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4,975,857 OR 4975857

LEXIS-NEXIS
Library: PATENT
File: JNLS

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1/4 LGST (1/1) - (C) LEGSTAT
PN - US 4975857 [US4975857]
AP - US 302332/89 19890127 [1989US-0302332]
DT - US-P
ACT - 19890127 US/AE-A
APPLICATION DATA (PATENT)
{US 302332/89 19890127 [1989US-0302332]}
- 19890405 US/AS02
ASSIGNMENT OF ASSIGNOR'S INTEREST
HITACHI, LTD., 6, KANDA SURUGADAI 4-CHOME, CHIYODA-KU, TOKYO, JAPAN, A
CORP. OF * KATSURA, KOYO : 19890222; KOJIMA, SHINICHI : 19890222;
KURAKAMI, NORIYUKI : 19890222
- 19901204 US/A
PATENT
- 19930713 US/RF
REISSUE APPLICATION FILED
921203
- 20000627 US/RF
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20000328
UP - 2000-27

2/4 CRXX (1/1) - (C) CLAIMS/RRX
AN - 2100971
PN - 4,975,857 A 19901204 [US4975857]
PA - Hitachi Ltd JP
PT - E (Electrical)
ACT - 19921203 REISSUE REQUESTED
Issue Date of O.G.: 19930713
Reissue Request Number: 07/985141
Examination Group responsible for Reissue process: 2301

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UACT- 2000-06-27

3/4 PAST (1/2) - (C) PAST
AN - 200026-001307
PN - 4975857 A [US4975857]
DT - A (UTILITY)
OG - 2000-06-27
CO - REA
ACT - REISSUE APPLICATION FILED
SH - REISSUE APPLICATION FILED

4/4 PAST (2/2) - (C) PAST
AN - 199327-000963
PN - 4975857 A [US4975857]
DT - A (UTILITY)
OG - 1993-07-13
CO - REA
ACT - REISSUE APPLICATION FILED

SH - REISSUE APPLICATION FILED